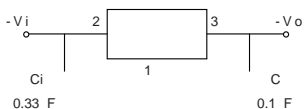


Parameter	Symbol	Min	Max	Unit
Input Voltage	V_i			V
Thermal Resistance Junction	R_{JA}			/W
Operating Junction Temperature Range	T_{OPR}			
Storage Temperature Range	T_{STG}	-65	150	

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit	
Output Voltage	V_o		25	-8.0	-8.32	V	
		-10.5V to 23V, $I_o=1mA \sim 40mA$	0-15	-7.6	-8.0	-8.4	V
		$I_o=1mA \sim 70mA$		-7.6	-8.0	-8.4	V
Load Regulation	V_o	$I_o=1mA \sim 100mA$	25	30	100	mV	
		$I_o=1mA \sim 40mA$	25	15	50	mV	
Line Regulation	V_o	-10.5V to 23V	25	42	200	mV	
		-11V to 23V	25	36	150	mV	
Quiescent Current	I_q		25	4	6	mA	
Quiescent Current Change	I_q	-11V to 23V	0-1		1.5	mA	
	I_q	1mA to 40mA	0-15		0.1	mA	
Output Noise Voltage	V_N	10Hz to 100KHz	25	54		V/V _o	
Ripple Rejection	RR	-11V to 21V, f=120Hz	0-1	37	46	dB	
Dropout Voltage	V_d		25	1.7		V	

* Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

